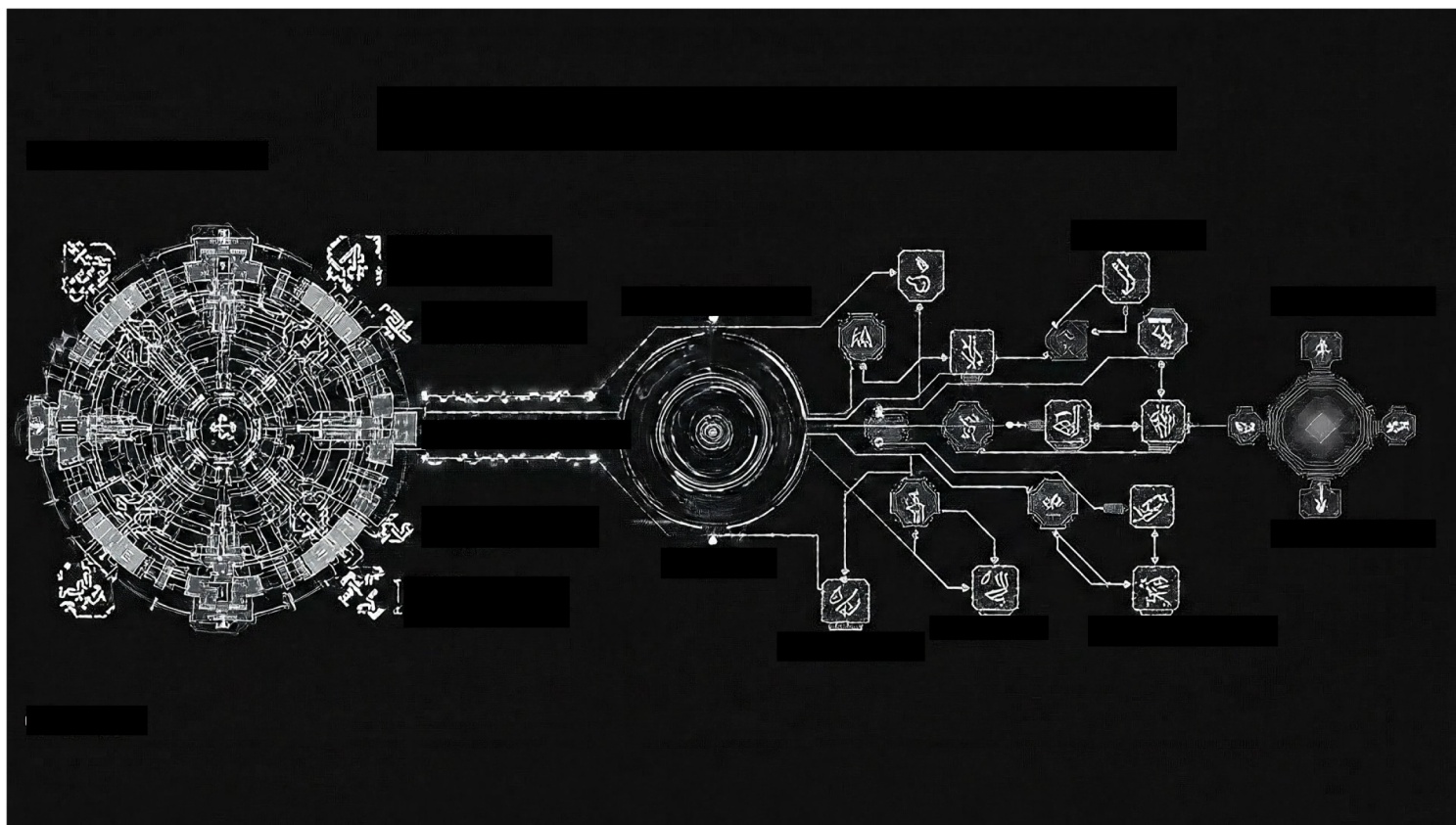




1. The diagram illustrates a system architecture where a central processing unit (CPU) is connected to a peripheral device (PD) via a communication bus (CB). The CPU is represented by the large circular structure on the left, and the PD is represented by the smaller rectangular components on the right.

2. The system is designed to handle data flow between the CPU and the PD. The communication bus (CB) acts as a central hub for data exchange, allowing the CPU to send commands to the PD and receive data back.

3. The peripheral device (PD) is composed of several functional blocks, including a data input/output (DIO) unit, a control unit (CU), and a status monitoring unit (SMU). These blocks are interconnected to ensure efficient data processing and system operation.

4. The diagram also shows the power supply (PS) and grounding (GND) connections for the system. The PS provides the necessary electrical power to the CPU and PD, while the GND ensures proper system grounding and safety.

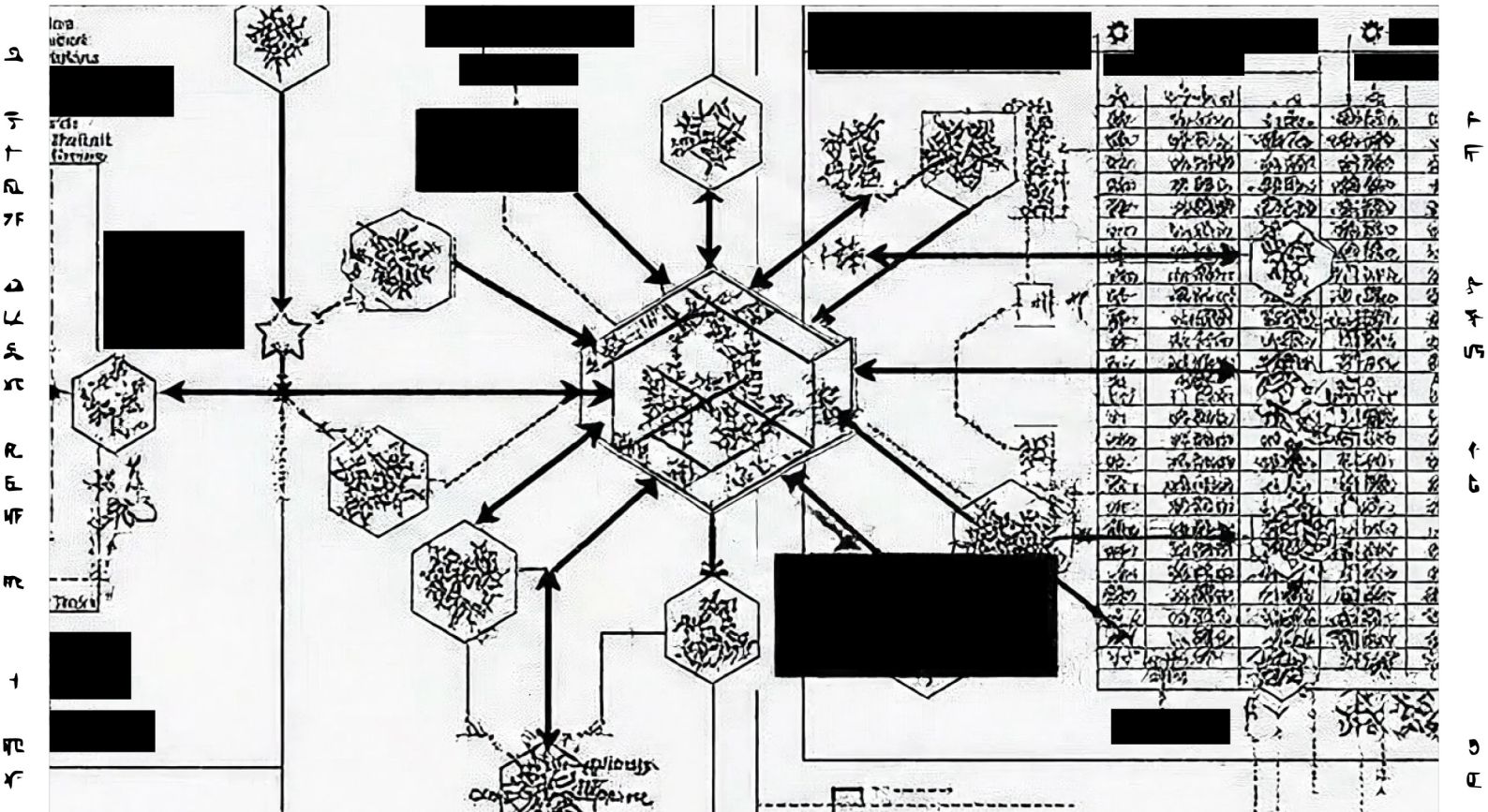
5. The overall system is designed for high reliability and performance, with redundant connections and fail-safe mechanisms in place to prevent data loss or system failure.



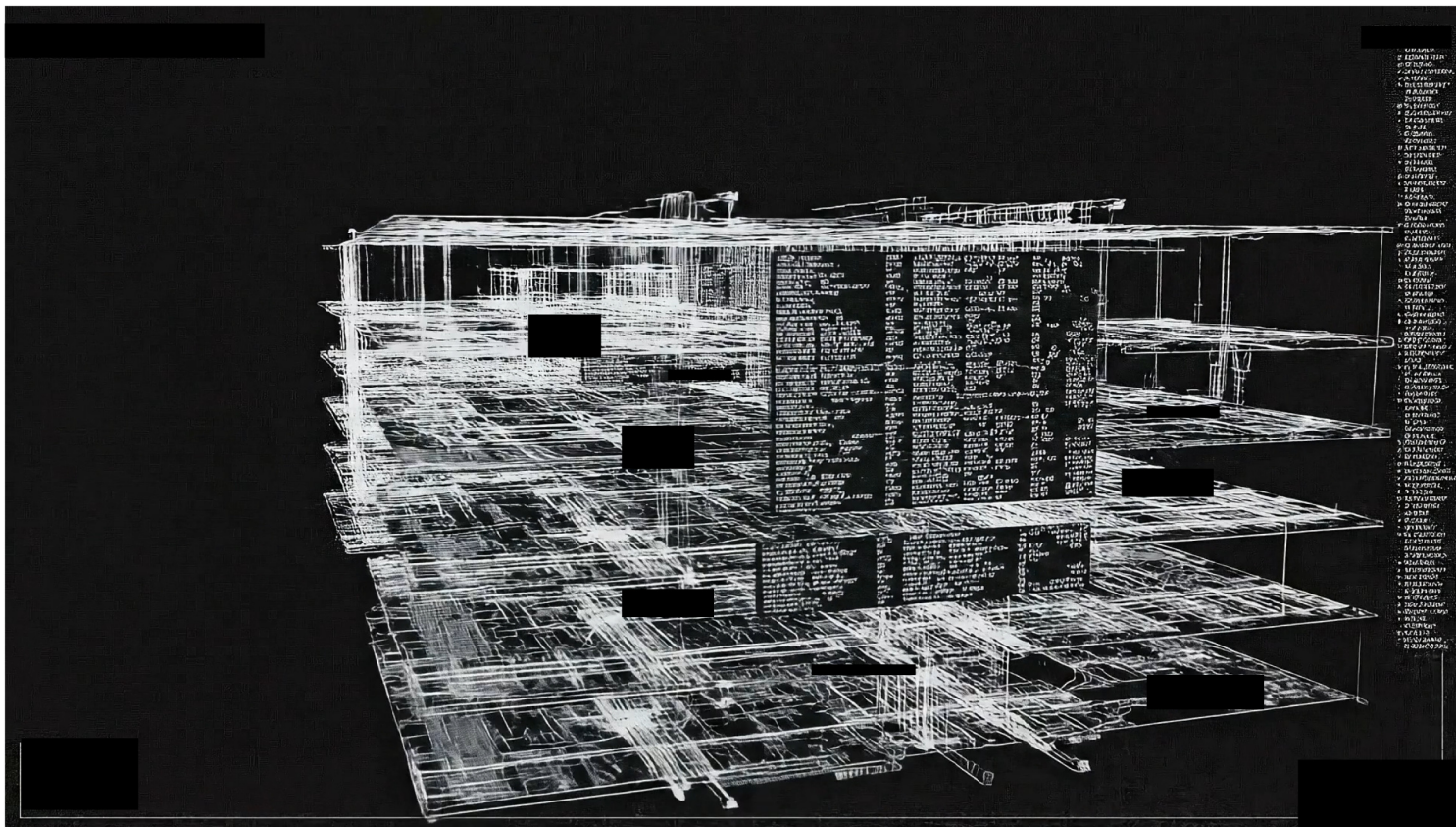


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Vertical text on the left side of the page, likely a list or index.

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